

TA110 VHDCI-100 Cable

1.2 m cable with one VHDCI-100 male connector and two male HD50 connector for TXMC635

Version 1.0

User Manual

Issue 1.0.0 April 2016



TA110-10R VHDCI-100 Cable

1.2 m cable with one 100 pin VHD male connector and two 50pin HD male connector for TXMC635

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Style Conventions

Hexadecimal characters are specified with prefix 0x, i.e. 0x029E (that means hexadecimal value 029E).

For signals on hardware products, an ,Active Low' is represented by the signal name with # following, i.e. IP_RESET#.

Access terms are described as:

W Write Only
R Read Only
R/W Read/Write
R/C Read/Clear
R/S Read/Set

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1 **Product Description**

The TA110 represents an adequate connection from a VHDCI-100 module connector to two standard HD50 connectors. On one side of the shielded cable a 100 pol. Honda HDRA connector is mounted. On the other side two HD50 (SCSI-2 type compatible) connectors provides a typical XMC/PMC used connection. The pin assembly is optimized for the connection to a TEWS TXMC635-xx reconfigurable FPGA with Digital and Analog I/O Interface.

The TA110 VHDCI-100 Cable in conjunction with two TA201-10 HD50 Terminal blocks could be used for prototyping and also for series.

Length of the TA110 VHDCI-100 Cable is at least 1.2 m.

The permissible maximum voltage for the TA110 is 30V DC.



2 Pin Assignment – I/O Connector

X1	X2		X1	X2
HDRA-E100M	PCS-XE50M		HDRA-E100M	PCS-XE50M
1	1	twisted	51	26
2	2	twisted	52	27
3	3	twisted	53	28
4	4	twisted	54	29
5	5	twisted	55	30
6	6	twisted	56	31
7	7	twisted	57	32
8	8	twisted	58	33
9	9	twisted	59	34
10	10	twisted	60	35
11	11	twisted	61	36
12	12	twisted	62	37
13	13	twisted	63	38
14	14	twisted	64	39
15	15	twisted	65	40
16	16	twisted	66	41
17	17	twisted	67	42
18	18	twisted	68	43
19	19	twisted	69	44
20	20	twisted	70	45
21	21	twisted	71	46
22	22	twisted	72	47
23	23	twisted	73	48
24	24	twisted	74	49
25	25	twisted	75	50

Table 2-1: Pin Assignment Part A (X1 to X2)



X1	X3		X1	X3
HDRA-E100M	PCS-XE50M		HDRA-E100M	PCS-XE50M
26	1	twisted	27	26
28	2	twisted	29	27
30	3	twisted	31	28
32	4	twisted	33	29
34	25	twisted with X1.84 / X3.50		/ X3.50
36	5	twisted	35	30
38	6	twisted	37	31
40	7	twisted	39	32
42	8	twisted	41	33
44	9	twisted	43	34
46	10	twisted	45	35
48	11	twisted	47	36
50	12	twisted	49	37
76	13	twisted	77	38
78	14	twisted	79	39
80	15	twisted	81	40
82	16	twisted	83	41
84 50		twisted with X1.34/ X3.25		
86	17	twisted	85	42
88	18	twisted	87	43
90	19	twisted	89	44
92	20	twisted	91	45
94	21	twisted	93	46
96	22	twisted	95	47
98	23	twisted	97	48
100	24	twisted	99	49

Table 2-2: Pin Assignment Part A (X1 to X3)



3 **Assembly Drawing**

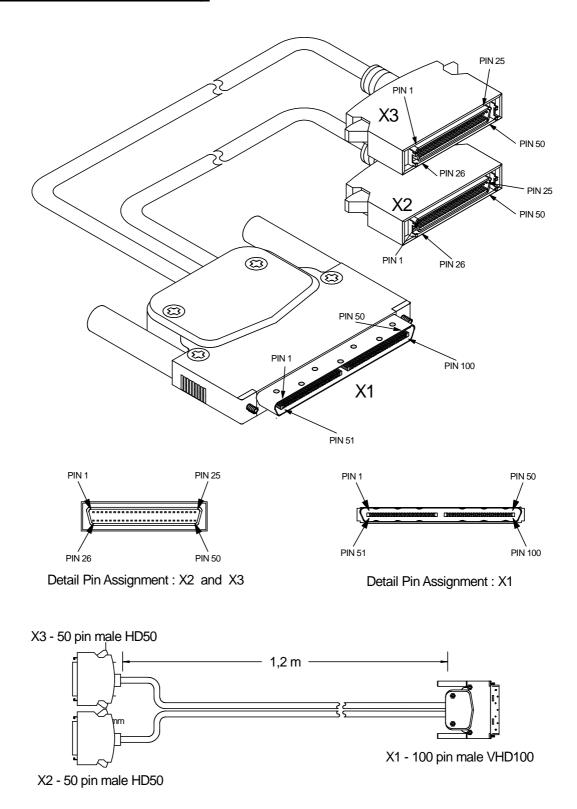


Figure 3-1: Assembly Drawing