

TA311

VHDCI-100 Cable Kit

Cable Kit for TXMC635 Modules

Version 1.0

User Manual

Issue 1.0.0

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TA311-10R

Cable Kit for Modules TXMC635

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Style Conventions

Hexadecimal characters are specified with prefix 0x, i.e. 0x029E (that means hexadecimal value 029E).

For signals on hardware products, an 'Active Low' is represented by the signal name with # following, i.e. IP_RESET#.

Access terms are described as:

W	Write Only
R	Read Only
R/W	Read/Write
R/C	Read/Clear
R/S	Read/Set

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1 Product Description

The TA311 combines two TA201 HD50 Terminal Blocks and the TA110 VHDCI-100 cable. It is used to build a standard interface for a switch cabinet to connect TEWS modules with other system devices. The VHDCI100 Cable Kit is therefore an essential wiring interface for prototyping and in the same way for machines and peripheral equipment.

The 100 pin Cable connection could be used for TXMC635 with a VHDCI100 female connector. At one side of the shielded cable a 100 pol. Honda HDRA connector is mounted which is used for connection to the TXMC635 module. At the other side of the cable two 50 pol. HD50 (SCSI-2 type compatible) connectors provides the connection to the TA201 HD50 Terminal Blocks.

The permissible maximum voltage for the TA307 is 30V DC.

Length of the TA110 VHDCI100 Cable is at least 1.2 m.

The HD50 Terminal Block has a universal socket and may simply mount on standard EM mounting rails as a compact terminal strip. Screw connections of the used terminal block have a nominal cross section of 2.5 mm².

2 Pin Assignment

2.1 TA110 VHDCI-100 Cable

The TA110 VHDCI-100 Cable Pin Assignment is separated into two parts. One part for X2 HD50 connector and one part for X3 HD50 Connector.

X1 HDRA-E100M	X2 PCS-XE50M		X1 HDRA-E100M	X2 PCS-XE50M
1	1	twisted	51	26
2	2	twisted	52	27
3	3	twisted	53	28
4	4	twisted	54	29
5	5	twisted	55	30
6	6	twisted	56	31
7	7	twisted	57	32
8	8	twisted	58	33
9	9	twisted	59	34
10	10	twisted	60	35
11	11	twisted	61	36
12	12	twisted	62	37
13	13	twisted	63	38
14	14	twisted	64	39
15	15	twisted	65	40
16	16	twisted	66	41
17	17	twisted	67	42
18	18	twisted	68	43
19	19	twisted	69	44
20	20	twisted	70	45
21	21	twisted	71	46
22	22	twisted	72	47
23	23	twisted	73	48
24	24	twisted	74	49
25	25	twisted	75	50

Table 2-1: Pin Assignment Part A (X1 to X2)

X1 HDRA-E100M	X3 PCS-XE50M		X1 HDRA-E100M	X3 PCS-XE50M
26	1	twisted	27	26
28	2	twisted	29	27
30	3	twisted	31	28
32	4	twisted	33	29
34	25	twisted with X1.84 / X3.50		
36	5	twisted	35	30
38	6	twisted	37	31
40	7	twisted	39	32
42	8	twisted	41	33
44	9	twisted	43	34
46	10	twisted	45	35
48	11	twisted	47	36
50	12	twisted	49	37
76	13	twisted	77	38
78	14	twisted	79	39
80	15	twisted	81	40
82	16	twisted	83	41
84	50	twisted with X1.34/ X3.25		
86	17	twisted	85	42
88	18	twisted	87	43
90	19	twisted	89	44
92	20	twisted	91	45
94	21	twisted	93	46
96	22	twisted	95	47
98	23	twisted	97	48
100	24	twisted	99	49

Table 2-2: Pin Assignment Part B (X1 to X3)

2.2 TA201 HD50 Terminal Block

On both HD50 Terminal Blocks the identically Pin Assignment is implemented.

Terminal Block	HD50 (SCSI-2)	Terminal Block	HD50 (SCSI-2)
1	1	26	26
2	2	27	27
3	3	28	28
4	4	29	29
5	5	30	30
6	6	31	31
7	7	32	32
8	8	33	33
9	9	34	34
10	10	35	35
11	11	36	36
12	12	37	37
13	13	38	38
14	14	39	39
15	15	40	40
16	16	41	41
17	17	42	42
18	18	43	43
19	19	44	44
20	20	45	45
21	21	46	46
22	22	47	47
23	23	48	48
24	24	49	49
25	25	50	50

Table 2-3 : HD50 Terminal Block Pin Assignment

Terminal Block	HD50 (SCSI-2)
1	Case ground / shield
2	Case ground / shield

Table 2-4 : Case Ground Terminal Block Pin Assignment

3 Assembly Drawing

3.1 TA110 VHDCI-100 Cable

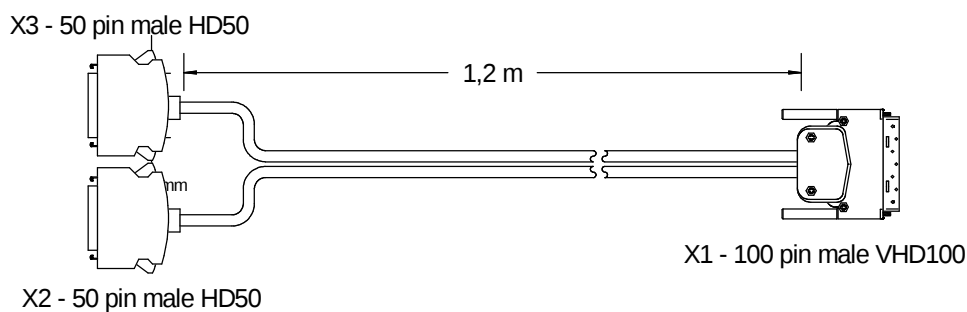
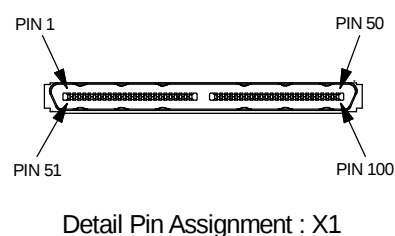
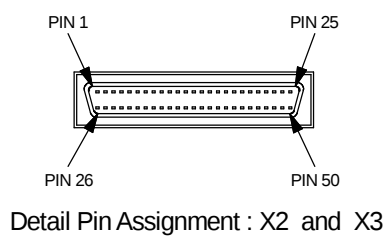
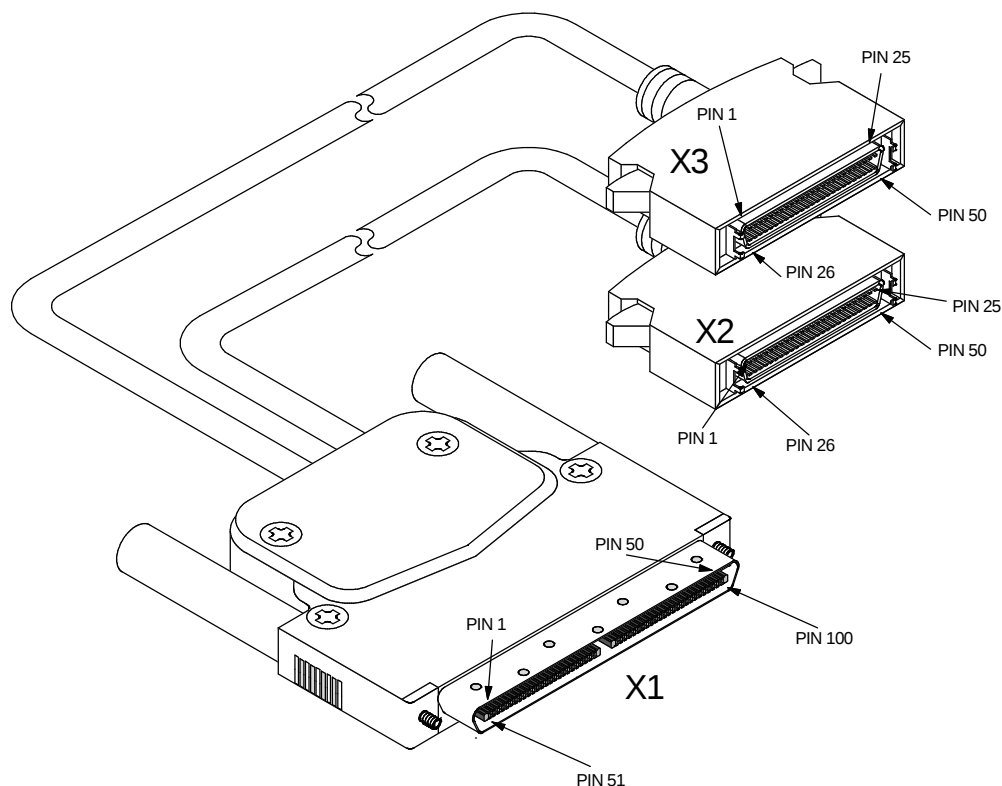


Figure 3-1 : Assembly Drawing TA110 VHDCI-100 Cable

3.2 TA201 HD50 Terminal Block

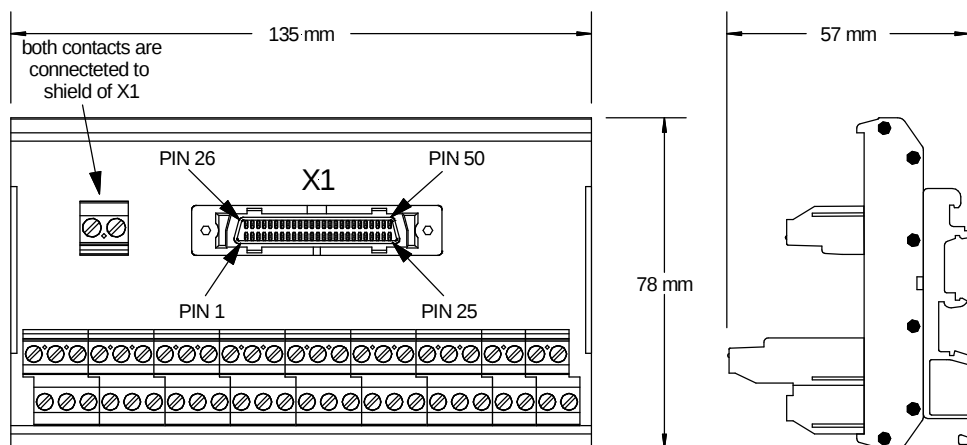
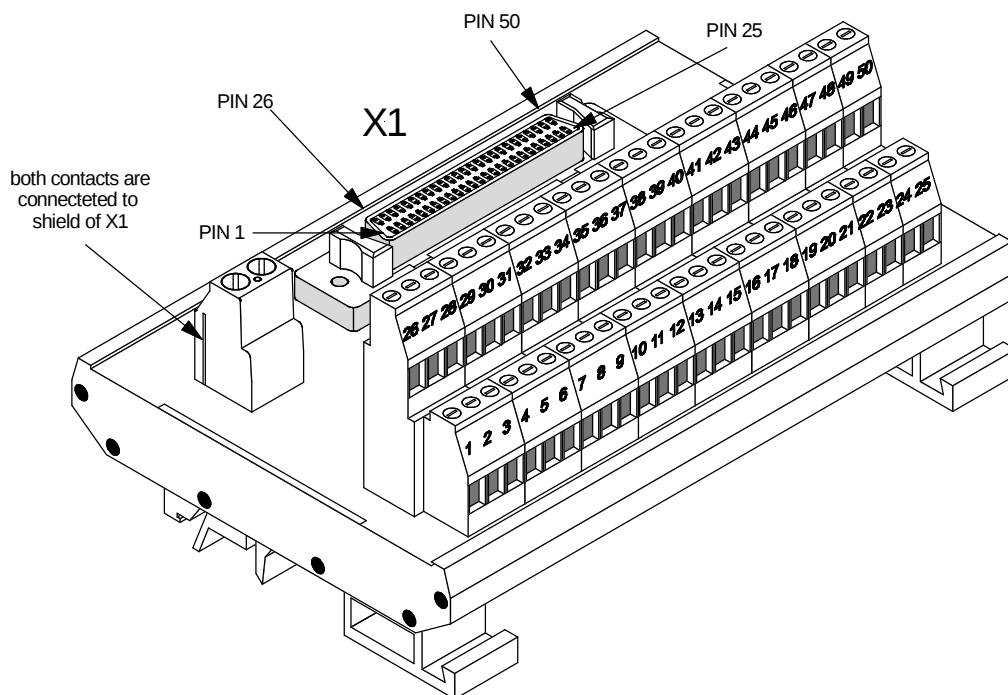


Figure 3-2 : Assembly Drawing TA201 HD50 Terminal Block