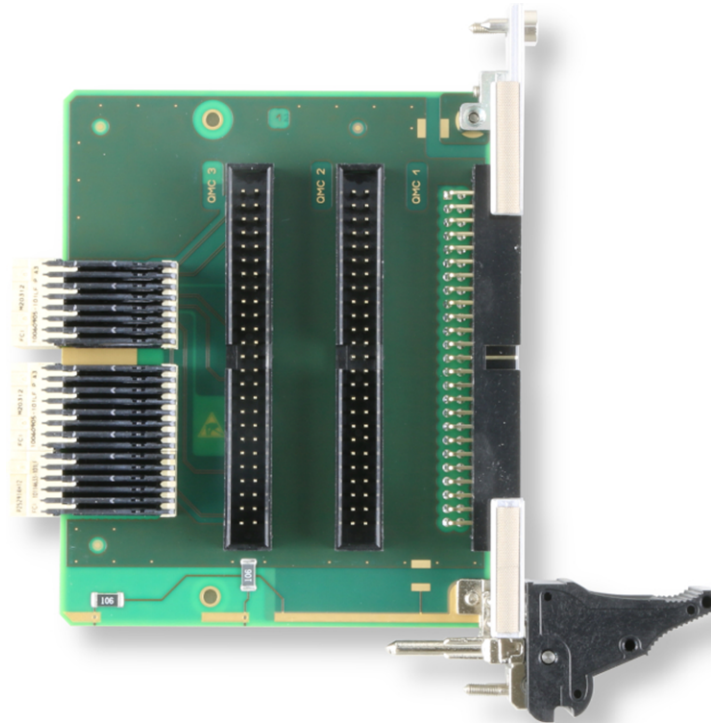


TCPS010-TM CPCI-S.0 Rear I/O Transition Module



TCPS010-TM-10R

Application Information

The TCPS010-TM is a PICMG CPCI-S.0 R3.0 compatible 3U rear I/O module.

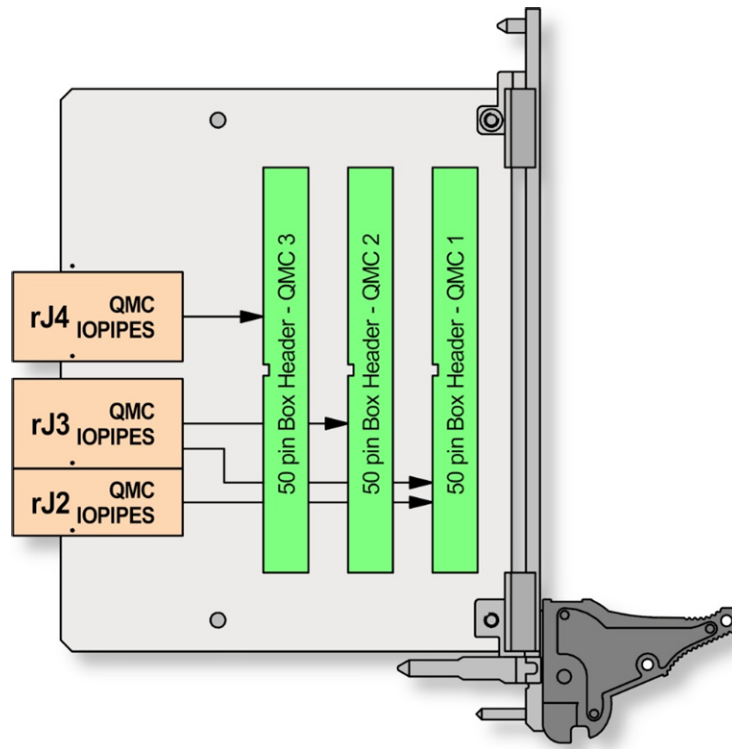
The TCPS010-TM distributes the QMC IOPIPE signals of TEWS' QMC Carriers with rear I/O to three 50 pin ribbon cable box headers, compatible with TEWS standard cable

kits. This makes the TCPS010-TM an ideal solution for prototyping CPCI-S.0 systems with QMC rear I/O, or it can be used as a base for custom rear-I/O solutions.

The operating temperature is -40°C to +85°C.

Technical Information

- Form Factor: CPCI-S.0 Rear Board
 - Board size: 100 mm x 80 mm
- J2, J3 & J4 I/O lines routed to 50 pin ribbon cable box headers
- The TCPS010-TM maintains IOPIPE isolation
- Operating temperature -40 °C to +85 °C
- MTBF (MIL-HDBK217F/FN2 GB 20 °C)
 - TCPS010-TM-10R: 910 000 h



Block Diagram

Order Information

RoHS Compliant

TCPS010-TM-10R CPCI-S.0 Rear I/O Transition Module

For the availability of non-RoHS compliant (lead solder) products please contact TEWS.

For the availability of conformal coated products please contact TEWS.

Related Products

TA305 Cable Kit for Modules with 50 pin Ribbon Cable Connector